

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,882,649 B1
DATED : April 19, 2005
INVENTOR(S) : Nils Gura and Hans Eberle

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1,

Line 5, "This copyright" should read -- The copyright --.

Column 5,

Line 33, "Schedule cycle 0: [1,0]," should read -- Schedule cycle 0: [0,0], --.

Line 34, "Schedule cycle 2: [1,0]," should read -- Schedule cycle 1: [1,0], --.

Column 8,

Line 16, "R[I+res, J+res]=I" should read -- R[I+res, J+res]=1 --.

Column 9,

Line 45, "input port and input port" should read -- input port 1 and input port --.

Column 10,

Line 8, "i(i=0 ... n-1):" should read -- i(i=0...n-1): --.

Column 11,

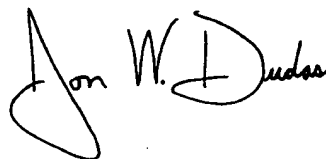
Line 21, "J: 0..MaxReq-1;" should read -- J: 0..MaxRes-1; --.

Column 12,

Line 60, "mod MaxReq:" should read -- mod MaxReq; --.

Signed and Sealed this

Fifth Day of July, 2005



JON W. DUDAS
Director of the United States Patent and Trademark Office